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[CITATION] [Compact dispatch tables for dynamically typed object oriented languages - all 6 versions »](#)

J Vitek, RN Horspool - Proceedings of the 6th International Conference on Compiler ..., 1996 - Springer

... account a one cycle load latency, a look-up requires TST ... compress dis- patch tables

by merging all tables into one ... The goal is to fred a partition of all rows ...

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[Instruction set selection for ASIP design - all 17 versions »](#)

M Gschwind - Proceedings of the seventh international workshop on ..., 1999 - portal.acm.org

... In our work, the hardware partition is not in a coprocessor, but the ... In addition,

the tag table lookup instruction primitive (18) can also be used in other ...

Cited by 44 - [Related Articles](#) - [Web Search](#)

[Virtual memory addressing device - all 2 versions »](#)

JA Anderson, RV Van Gelder, LF Yazolano, JE Braun - US Patent 4,376,297, 1983 - Google Patents

... here that the double-fetch, table lookup address translation ... for bypassing said table

look up operation for a ... in combination: auser partition storage register ...

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[Matching partition a linked list and its optimization - all 4 versions »](#)

Y Han - Proceedings of the first annual ACM symposium on Parallel ..., 1989 - portal.acm.org

... for computing the • 2) matching partition funcaonjl (a ... One may suggest building such

an instruction into the ... alternative is to use a lookup table to convert ...

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[The FFT on a GPU](#)

K Moreland, E Angel - Proceedings of the ACM SIGGRAPH/EUROGRAPHICS conference on ..., 2003 - portal.acm.org

... In general, at iteration i we have $N/2^i$ partitions of size 2^i . Let $P_{i,p}[u]$

be the u th entry of the p th partition at iteration i . The sequence $P_{i,p}$...

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[Efficient table access for reversible variable-length decoding - all 3 versions »](#)

JLH Webb, TI Inc, TX Dallas - Circuits and Systems for Video Technology, IEEE Transactions ..., 2001 - ieeexplore.ieee.org

... Either search returns an index, which is used to look up run-level-last values. ... Using

the number of leading zeros to partition the table, let index (1) ...

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[Method and apparatus for shortening display list instructions - all 4 versions »](#)

G Devic - US Patent 6,072,568, 2000 - freepatentsonline.com

... the present invention, the partition lookup table 240 comprises ... is addressed by a

partition data bit ... its basic implementation, the look-up table 240 contains ...

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[PDF] [Locking and Latching in a Memory-Resident Database System - all 8 versions »](#)

V Gottemukkala, TJ Lehman - Proceedings of the 13th International Conference on Very ..., 1992 - sigmod.org

... repeatedly, suggesting that the instructions and data for the latch ... Table 1); there

is little difference between table latching and partition latching ...

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[PDF] [An Instruction Stream Compression Technique - all 5 versions »](#)

P Bird, T Mudge - Electrical Engineering and Computer Science Dept., Univ. of ..., 1996 - eeecs.umich.edu

... Partition the ROM table into sets of sequences. ... Use an index table to indicate ... where

the 1st instruction of the sequence is a part of the index lookup. ...

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[... number of hits in a translation lookaside buffer including instruction address lookaside register - all 3 versions »](#)

R Becker, P Mehring... - US Patent 5,305,444, 1994 - freepatentsonline.com

... table (PLAT) for early partition assignment identification. ... If translation through

the page lookup tables is required ... 1 0, each instruction address translation ...

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Key authors: [M Gschwind](#) - [K Moreland](#) - [V Gottemukkala](#) - [J Vitek](#) - [P Ranganathan](#)

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